

Figure 1a. Diagram of Universal VLD

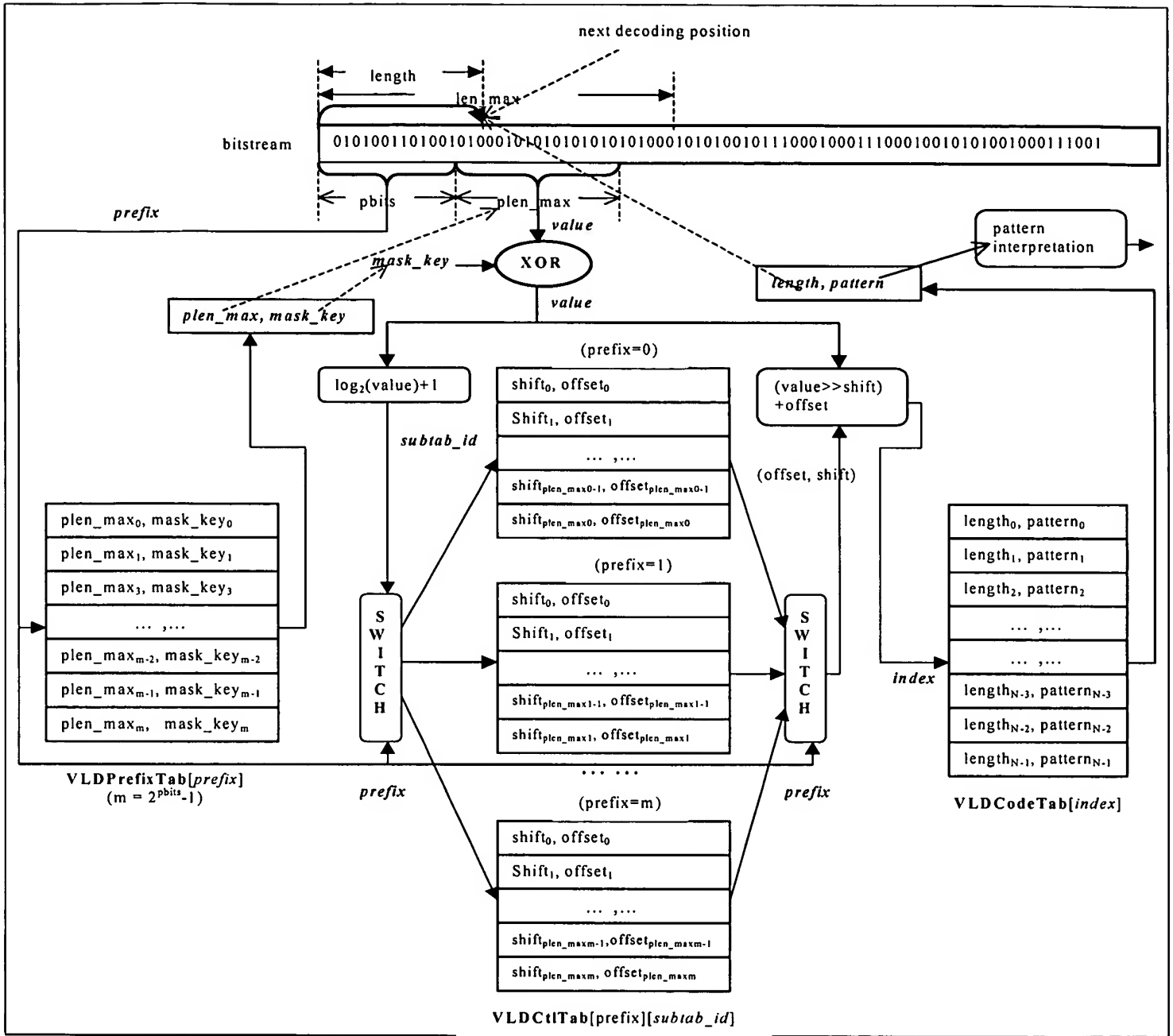


Figure 1b. Principle of Advanced Universal VLD

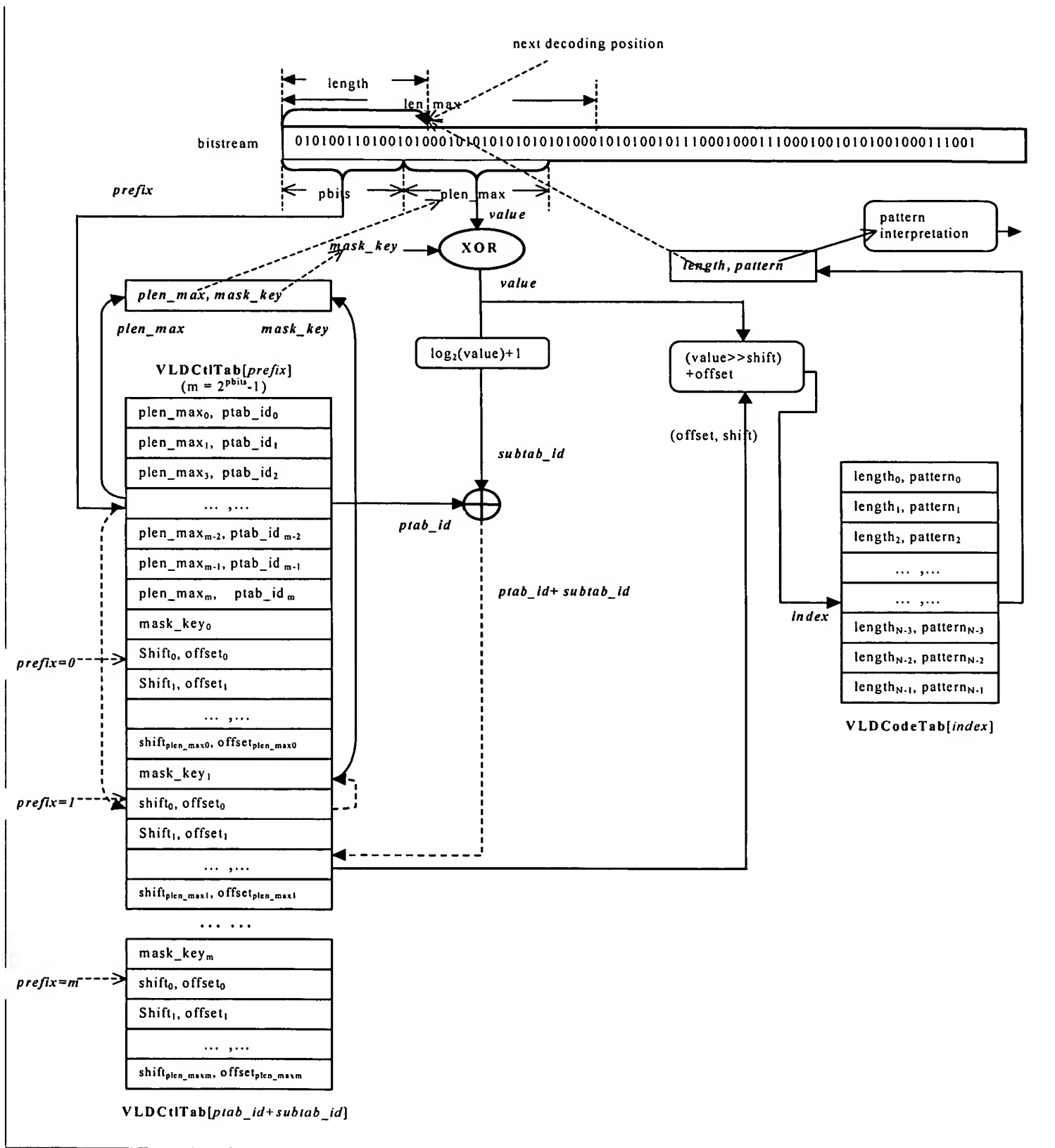


Figure 1c

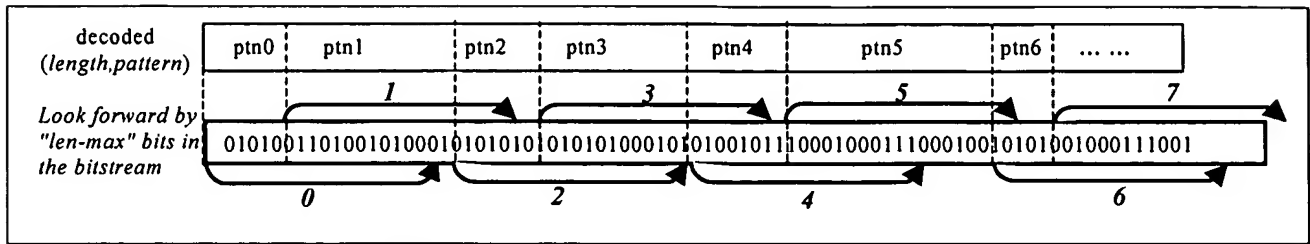


Figure 2. Principle of Variable Length Decoding

Figure 3a block diagram of programmable hardware for universal VLD

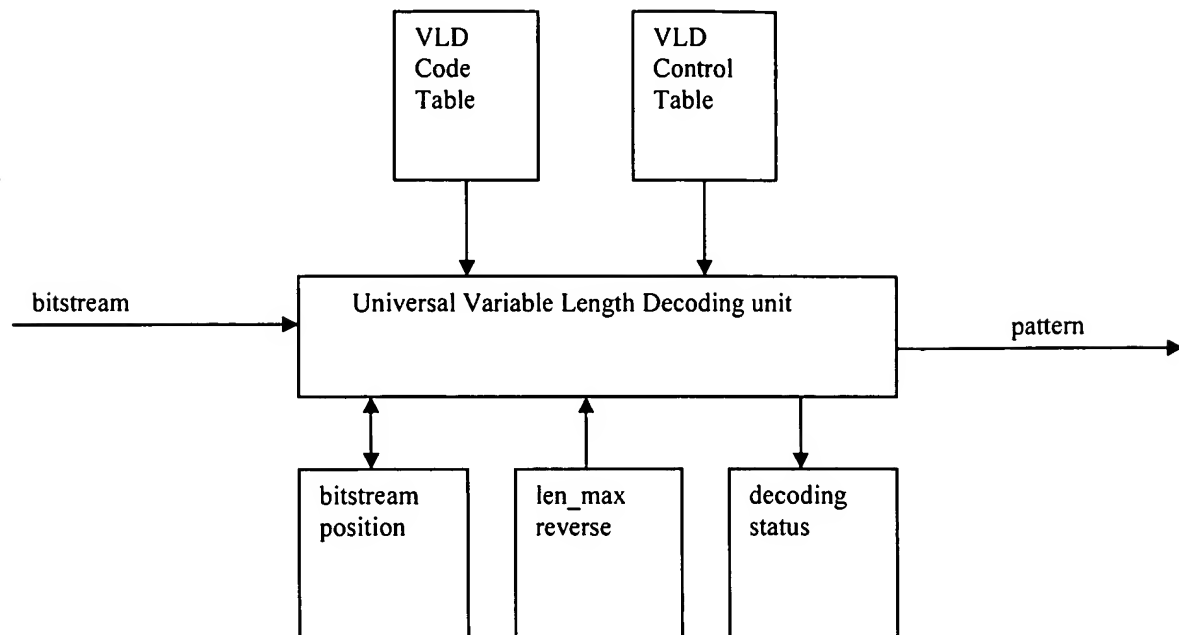


Figure 3b block diagram of programmable hardware for advanced universal VLD

